

#1 IDS
1/6/4PTO/SB/08a/b (08-03)
Approved for use through 07/31/2006. OMB 0651-0031
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Substitute for form 1449A/B/PTO			Complete if Known		
			Application Number	NOT YET ASSIGNED	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)			Filing Date	January 6, 2004	
			First Named Inventor	Hongmei Wang	
			Art Unit	N/A	
			Examiner Name	Not Yet Assigned	
Sheet	1	of	1	Attorney Docket Number	M4065.0536/P536-B

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
MWP	AA	5,340,759	08-1994	Hsieh et al.*	
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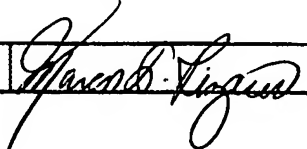
FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
MWP	CA	S.M. Sze, "Physics of Semiconductor Devices," John Wiley & Sons, New York, (1981), pp. 454-455.*	
	CB	Syd R. Wilson, et al., "Handbook of Multilevel Metallization for Integrated Circuits," Noyes Publ., Westwood, New Jersey, (1993), pp. 259-264.*	
	CC	Gary E. McGuire, "Semiconductor Materials and Process Technology Handbook," Noyes Publ., Norwich, New York, (1988), pp. 46-47.*	
	CD	Julian J. Sanchez, et al., "Drain-Engineered Hot-Electron-Resistant Device Structures: A Review," IEEE Trans. On Electron Devices, Vol. 36, No. 6, (1989), pp. 1125-1132.*	

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Examiner Signature		Date Considered	3/15/2005
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